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10/058,258	01/25/2002	Julie Ann Ward	10019700-1	4105

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EXAMINER

LIN, WEN TAI

ART UNIT	PAPER NUMBER
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2154

DATE MAILED: 03/21/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

10/058,258

Applicant(s)

WARD ET AL.

Examiner

Wen-Tai Lin

Art Unit

2154

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 05 July 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-26 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-26 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 25 January 2002 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 5/05-7/05.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

### DETAILED ACTION

1. Claims 1-26 are presented for examination.
2. Claim 13 is objected to because the term "the secondary path" appears to lack antecedent basis.

### *Claim Rejections - 35 USC § 102*

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 1-2, 7-11, 14-15 and 20-24 are rejected under 35 U.S.C. 102(e) as being anticipated by Ambiehl et al.[U.S. PGPub 20020122421].
5. As to claims 1-2, Ambiehl teaches the invention as claimed including: a computer implemented method for verifying a design for an interconnect fabric, the design including an arrangement of interconnect elements for interconnecting a plurality of network nodes and the

design having requirements for a plurality of flows among the network nodes, and for each of the plurality of flows [e.g., Abstract], the method comprising:

associating the flow with a path for the flow through the interconnect fabric [e.g., Fig. 1]; and for each interconnect element in each path, aggregating requirements associated with each of the corresponding flows and determining whether the aggregated requirements exceeds a capacity of the interconnect element [e.g., paragraphs 17, 75-81; Fig. 8].

6. As to claims 7-9, Ambiehl further teaches that the aggregated requirements include bandwidth requirements;

requirements of ports for each of the plurality of flows; and determining whether a number of available ports of one or more of the interconnect elements is exceeded by the aggregated requirements of ports [e.g., Abstract and paragraph 81; note that Ambiehl expresses the bandwidth requirements in terms of bit rates of packets transferring through the links].

7. As to claim 10, Ambiehl teaches that the method further comprising: determining whether a flow corresponds to a valid path through the interconnect fabric, a valid path starting at a source node for the flow; terminating at an end node for the flow; and passing through a contiguous subset of the interconnect elements [e.g., Abstract and paragraph 6].

8. As to claim 11, Ambiehl further teaches rejecting the design if it does not include a valid path for each flow [e.g., paragraph 17].

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9. As to claims 14-15 and 20-24, since the features of these claims can also be found in claims 1-2 and 7-11, they are rejected for the same reasons set forth in the rejection of claims 1-2 and 7-11 above.

***Claim Rejections - 35 USC § 103***

10. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

11. Claims 3-6 and 16-19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ambiehl et al.(hereafter "Ambiehl") [U.S. PGPub 20020122421], as applied to claims 1-2, 7-11, 14-15 and 20-24 above, further in view of Dere et al.(hereafter "Dere") [U.S. Pat. No. 5802286].

12. As to claim 3, Ambiehl teaches that an interconnection node (or device) in the packet switching network can be symbolizes as having a bank of input/output ports, multiplexer (which in essence behaves like a switch), and FIFO, respectively and switched Ethernet networks are very widespread example of packet switching network [paragraphs 6 and 10-14]. Ambiehl does not specifically teach that the interconnect devices include hubs.

However, in the same field of endeavor, Dere teaches that Ethernet switch, hub, and ATM switch are examples of physical devices that can be configured to create a virtual network [e.g., Dere: Fig.2].

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Ambiehl and Dere, and include hubs as network devices in Ambiehl's packet switching network because Dere's embodiments employ more realistic physical devices and its design procedure would broaden the use of Ambiehl's method (e.g., to include the very popular hub-oriented LAN environments in the design and verification processes).

13. As to claim 4, Ambiehl and Dere further teach that when the interconnect devices includes a hub, the method further comprises: identifying an extent of a domain of hub connected components [e.g., Dere: 360, 365, 350, Fig.3 and col.8, lines 52-65; col.9, lines 10-21].

14. As to claim 6, Ambiehl and Dere further teach that said identifying an extent of a domain of hub connected components comprises: constructing a tree data structure, wherein a hub occupies a position in the tree [e.g., 365, Fig.3; col.6, lines 40-49] and other interconnect elements connected to the hub occupy positions in the tree one level down from the hub [e.g., 630, 620, Fig.6; col.8, lines 52-65; note that following the construction of tree data structures in Figs. 7-9, it is by default that any of the node could be a hub and the interconnect one level down from the hub an interconnect element connected to the hub].

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15. As to claims 5 and 16-19, since the features of these claims can also be found in claims 1-4, 6 and 14-15, they are rejected for the same reasons set forth in the rejection of claims 1-4, 6 and 14-15 above.

16. Claims 12-13 and 25-26 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ambiehl et al.(hereafter "Ambiehl") [U.S. PGPub 20020122421], as applied to claims 1-11 and 14-24 above, further in view of Cowan et al.(hereafter "Cowan") [U.S. PGPub 20010039574].

17. As to claims 12-13, Ambiehl does not specifically teach providing a backup path for a primary path and associate both paths with the same flow to determine whether the design has capacity for each of the paths, so that when the backup path may take the place of the primary path if the latter fails.

However, Cowan teaches providing redundant paths for primary paths in consideration of fault-tolerance [e.g., Abstract; paragraphs 36, 52, 62].

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Ambiehl and Cowan by providing backup paths in Ambiehl's design and verification process (i.e., verifying that both the primary and backup paths satisfy the aggregated requirements) because providing redundant elements is a well-known method of boosting a network's reliability.

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18. As to claims 25-26, since the features of these claims can also be found in claims 1 and 12-14, they are rejected for the same reasons set forth in the rejection of claims 1 and 12-14 above.

19. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure: Westfall et al. [U.S. Pat. No. 6976087], which teaches network path validation to meet various service requirements.

20. A shortened statutory period for response to this action is set to expire 3 (three) months and 0 days from the mail date of this letter. Failure to respond within the period for response will result in ABANDONMENT of the application (see 35 U.S.C. 133, M.P.E.P. 710.02, 710.02(b)).

### ***Conclusion***

**Examiner note:** Examiner has cited particular columns and line numbers in the references as applied to the claims above for the convenience of the applicant. Although the specified citations are representative of the teachings of the art and are applied to the specific limitations within the individual claim, other passages and figures may apply as well. It is respectfully requested from the applicant in preparing responses, to fully consider the references in entirety as potentially teaching all or part of the claimed invention, as well as the context of the passage as taught by the prior art or disclosed by the Examiner.



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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Wen-Tai Lin whose telephone number is (571)272-3969. The examiner can normally be reached on Monday-Friday (8:00-5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Follansbee can be reached on (571)272-3964. The fax phone numbers for the organization where this application or proceeding is assigned are as follows:


(571) 273-8300 for official communications; and

(571) 273-3969 for status inquires draft communication.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Wen-Tai Lin

March 16, 2006

  
3/16/06